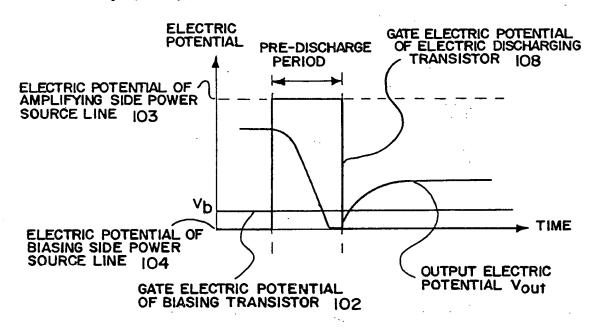
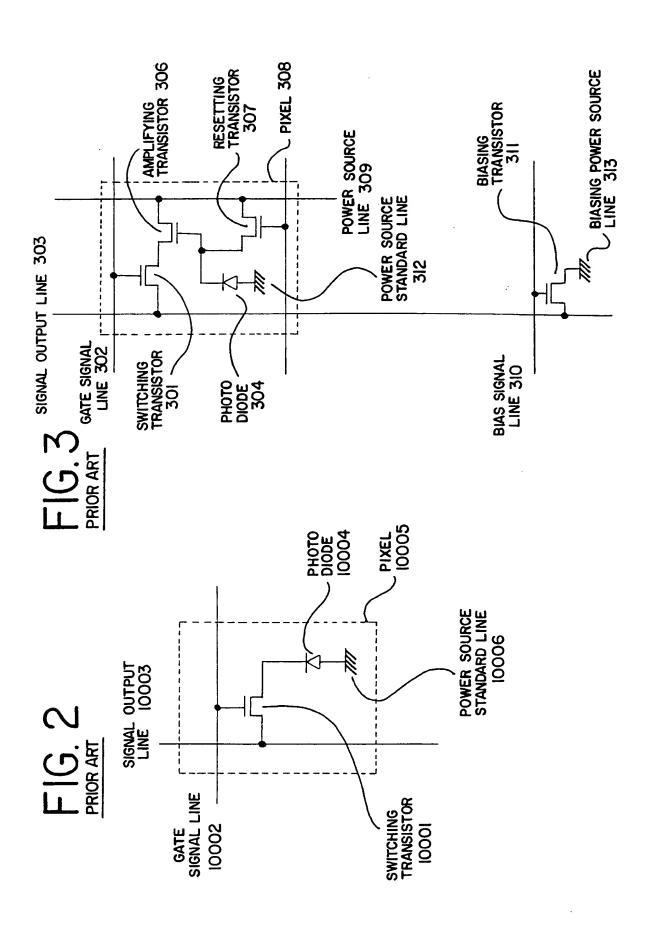
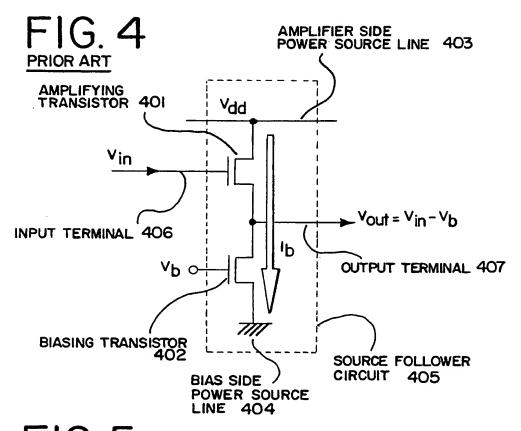
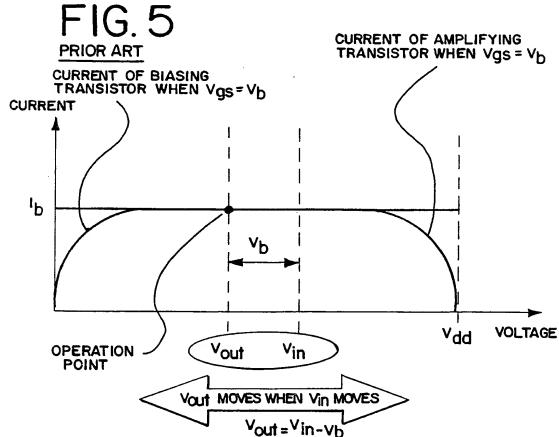


FIG. IB











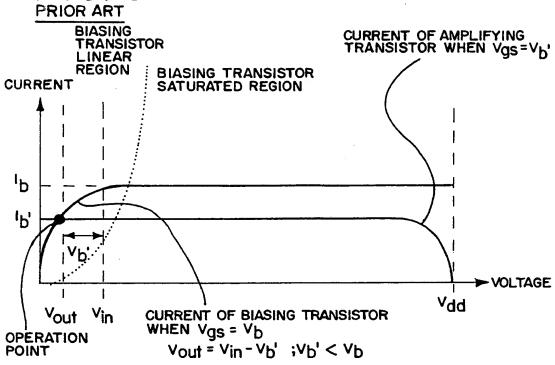
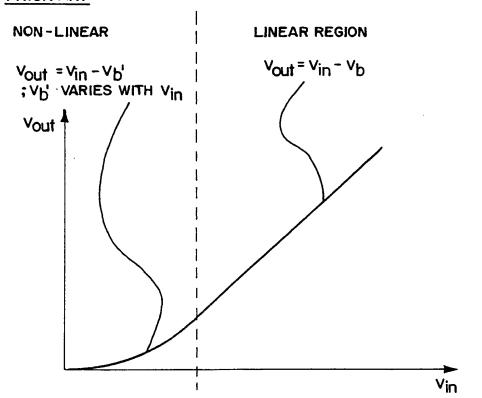
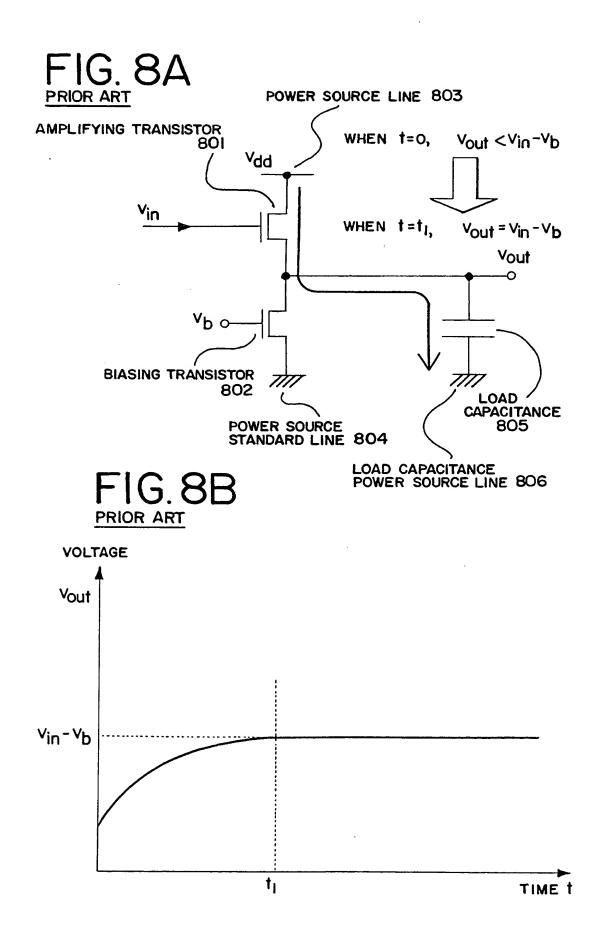
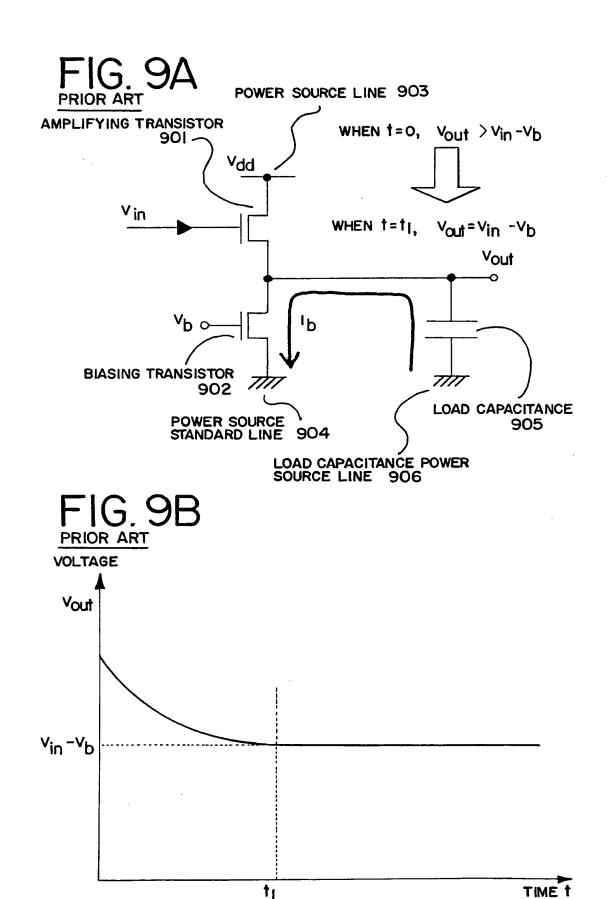


FIG. 7







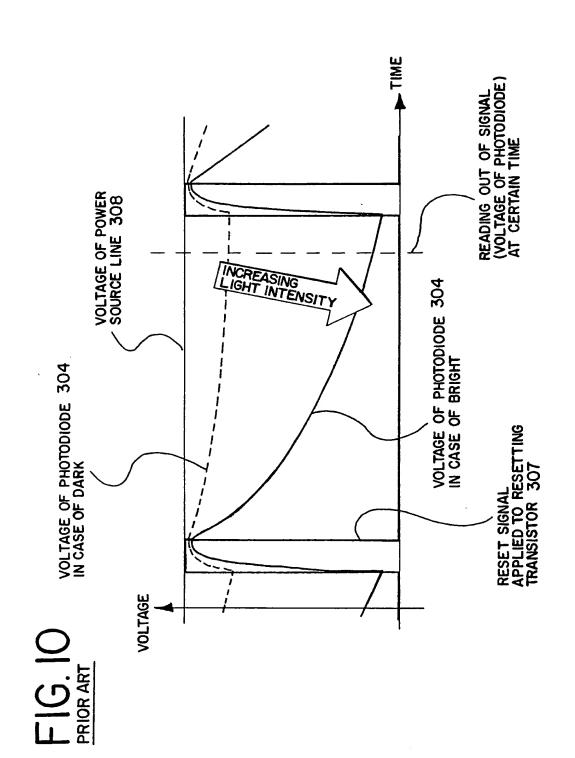


FIG. I IA

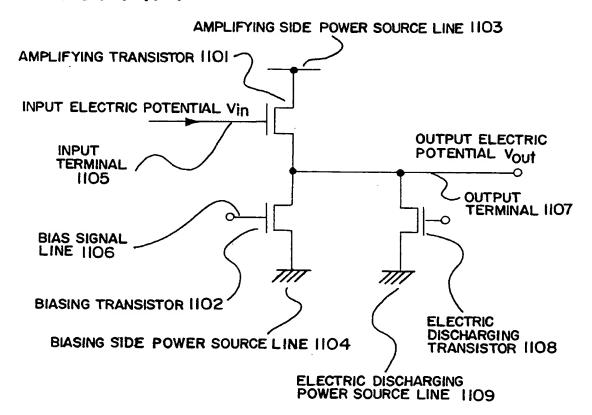
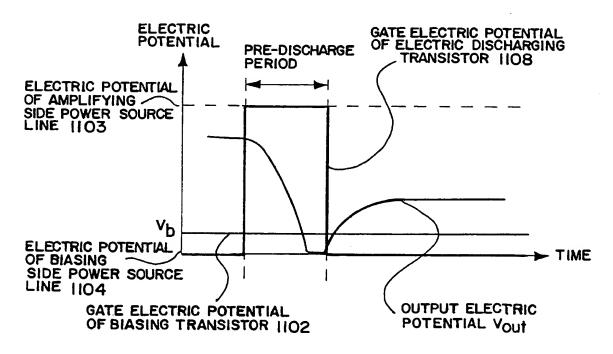


FIG. IIB



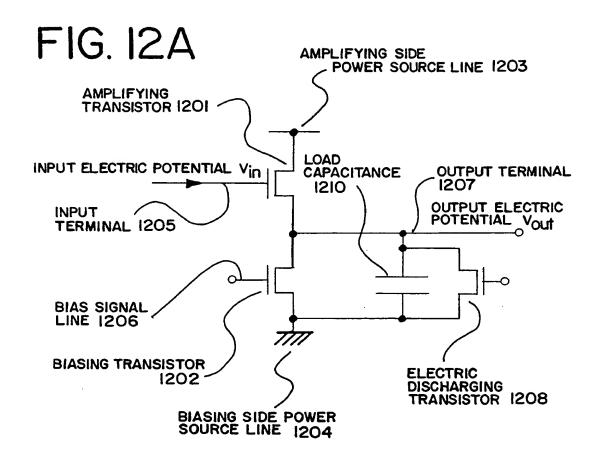
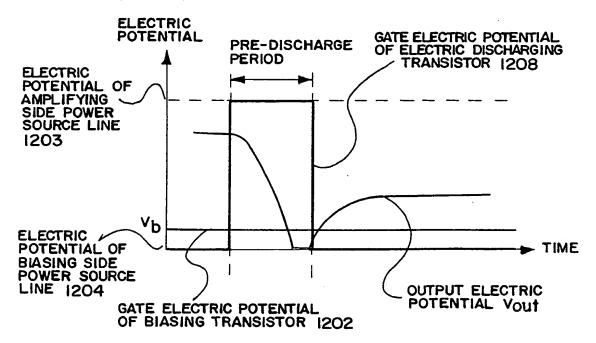


FIG. 12B



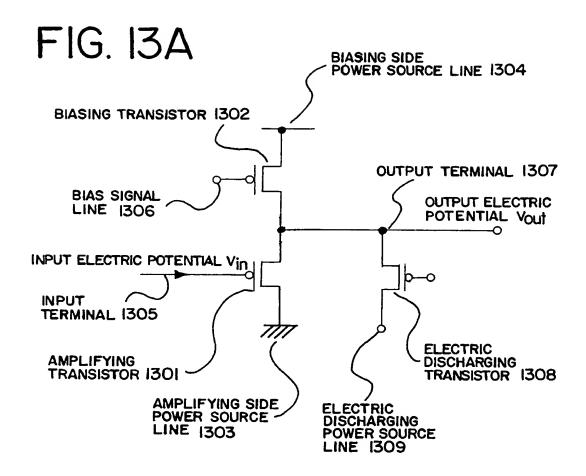
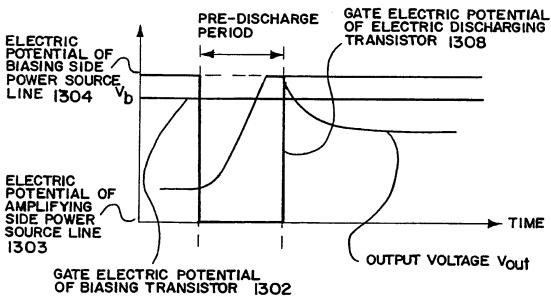


FIG. 13B

ELECTRIC POTENTIAL



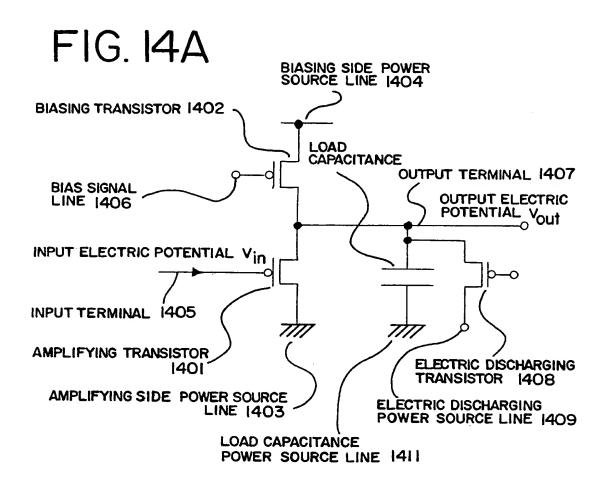
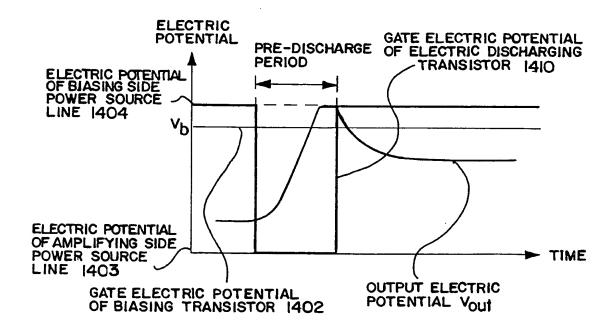


FIG. 14B



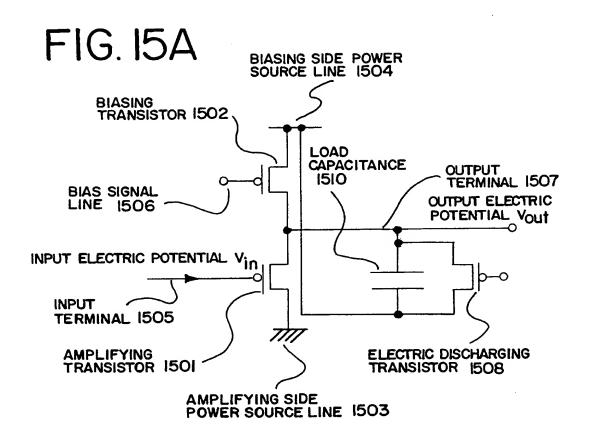
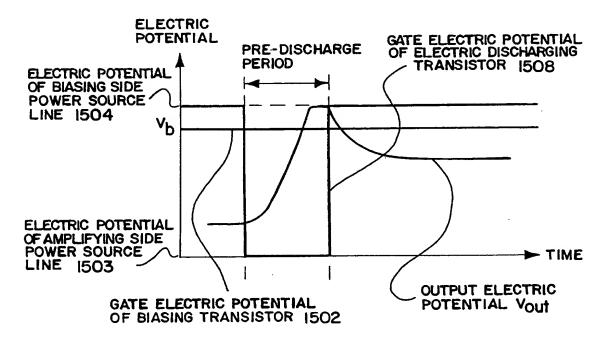
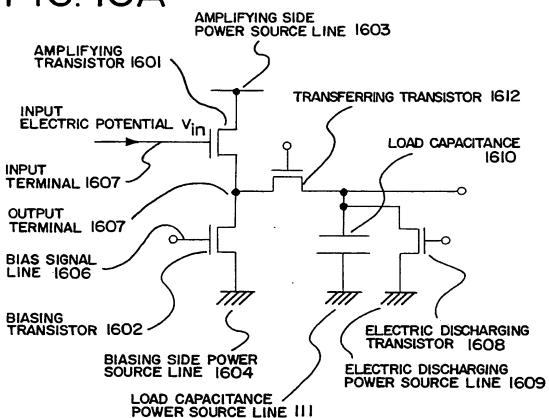


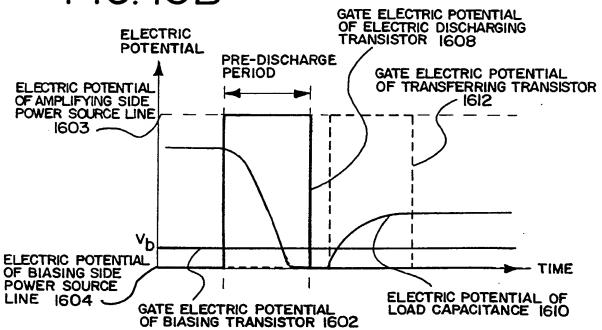
FIG. 15B

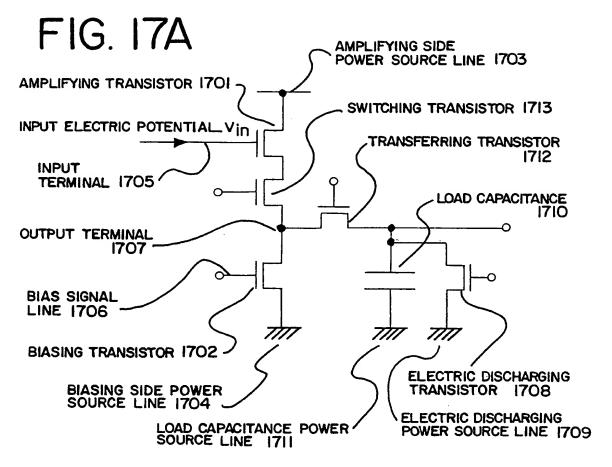




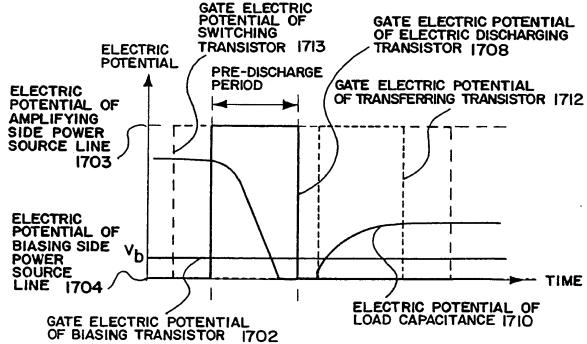












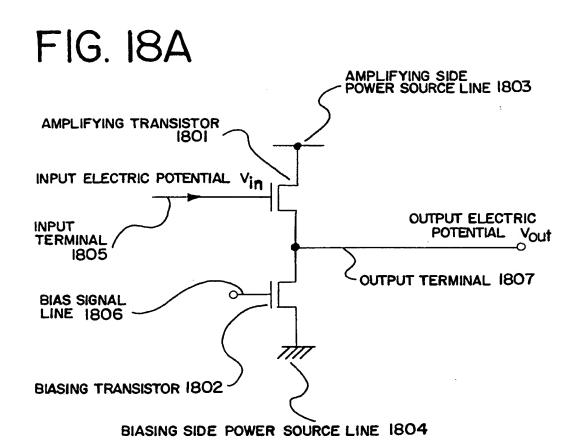


FIG. 18B

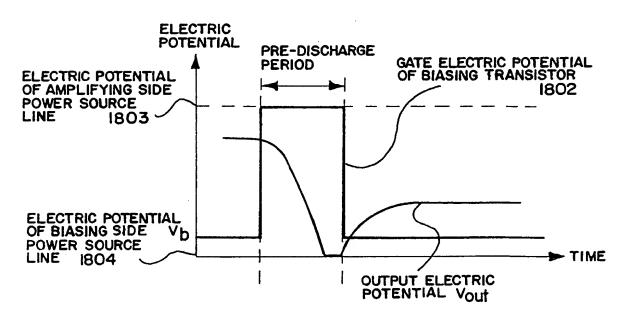


FIG. 19A

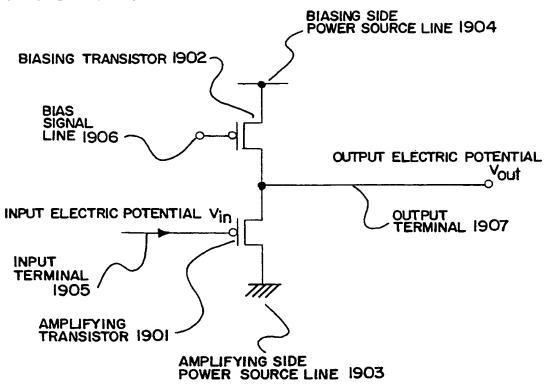


FIG. 19B

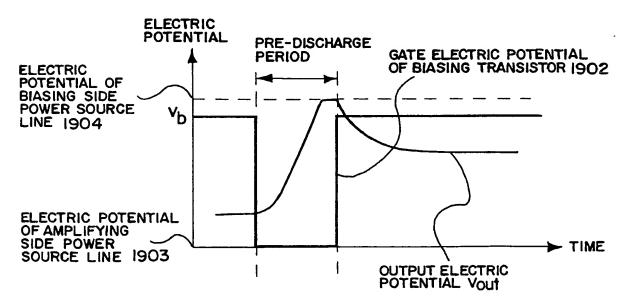
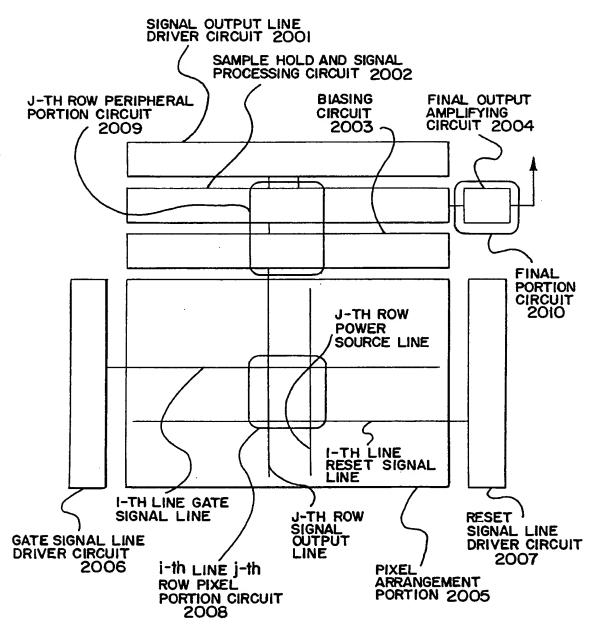


FIG. 20



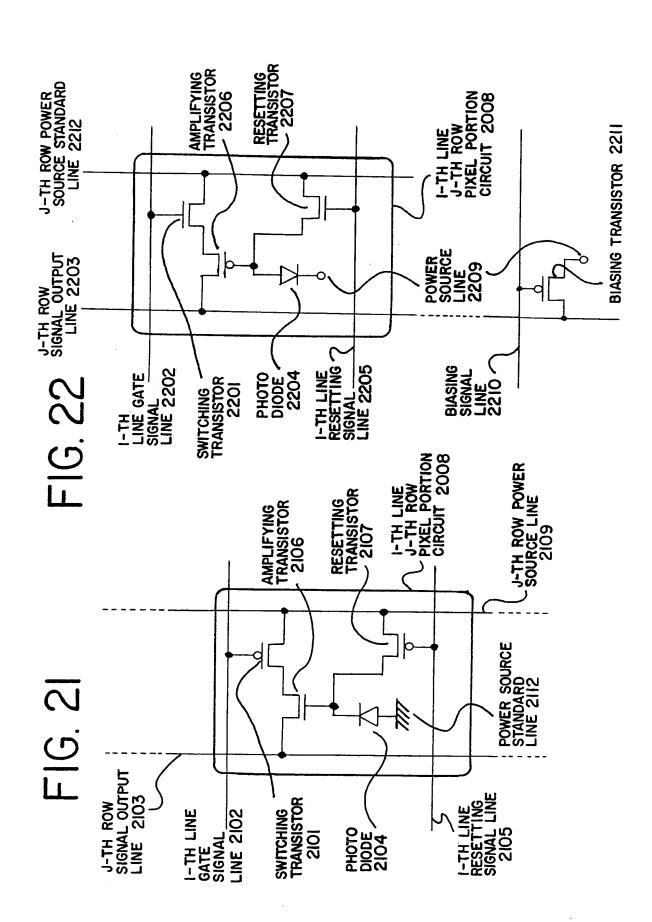


FIG. 23

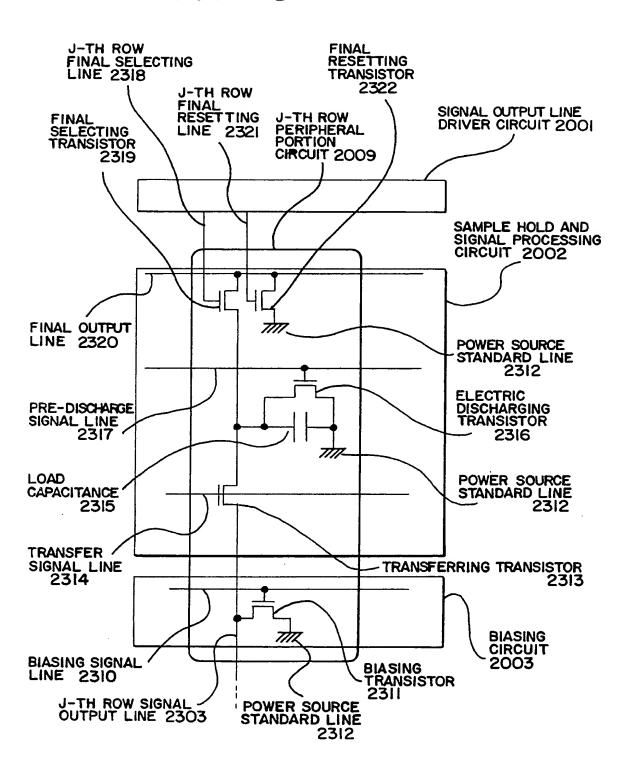


FIG. 24

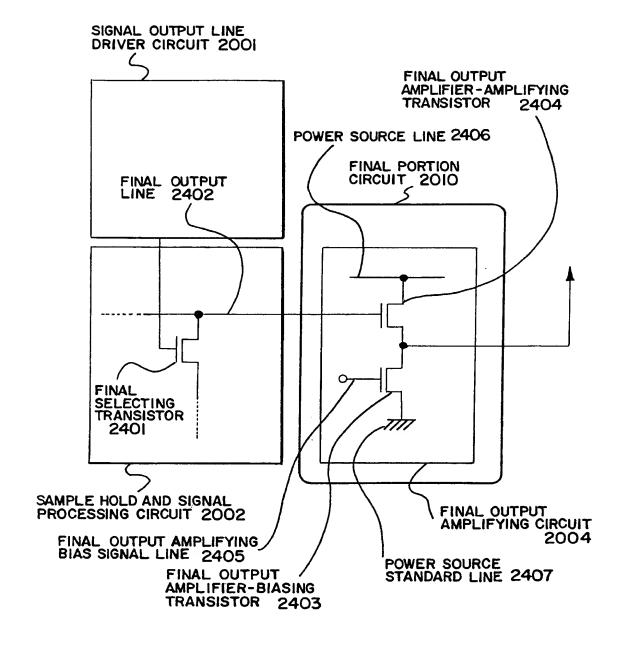


FIG. 25

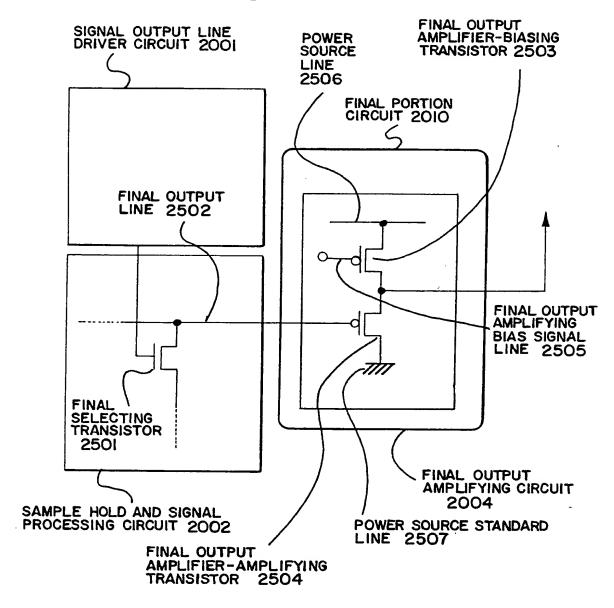
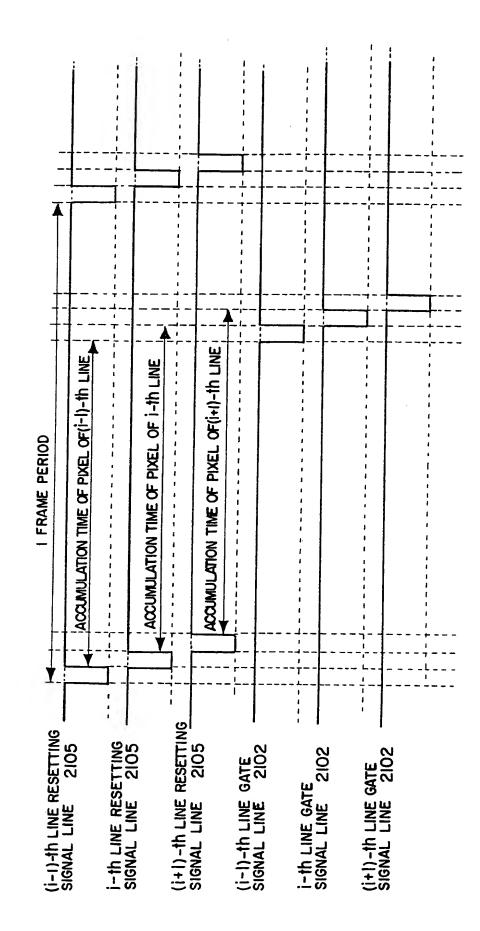
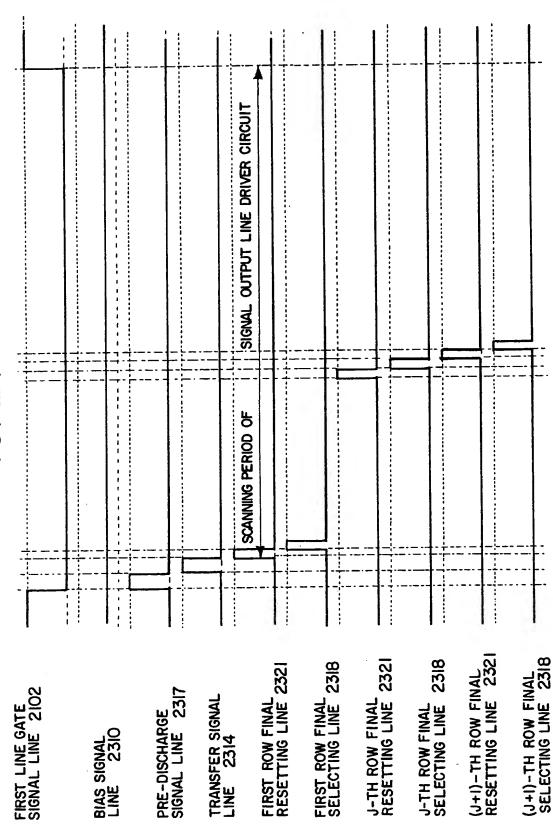


FIG. 26









BIAS SIGNAL LINE 2910

TRANSFER SIGNAL LINE 2914

FIRST ROW FINAL RESETTING LINE 2921

FIRST ROW FINAL SELECTING LINE 2918

J-TH ROW FINAL RESETTING LINE 2921

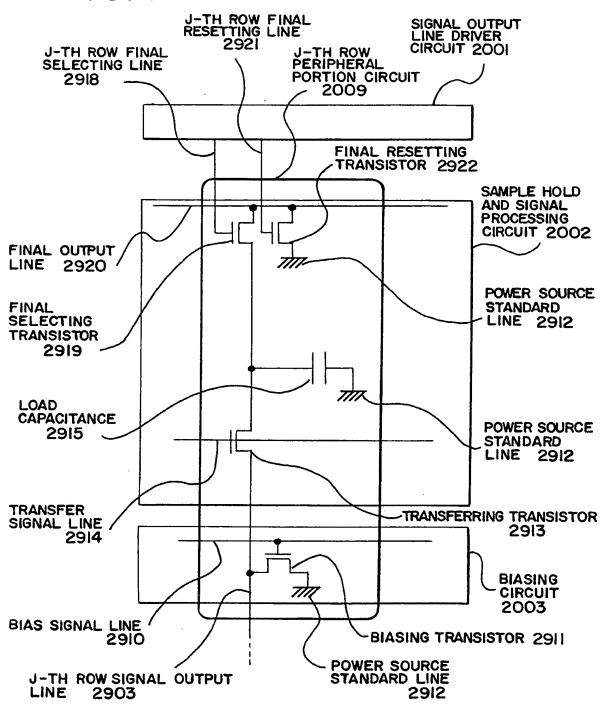
J-TH ROW FINAL SELECTING LINE 2918

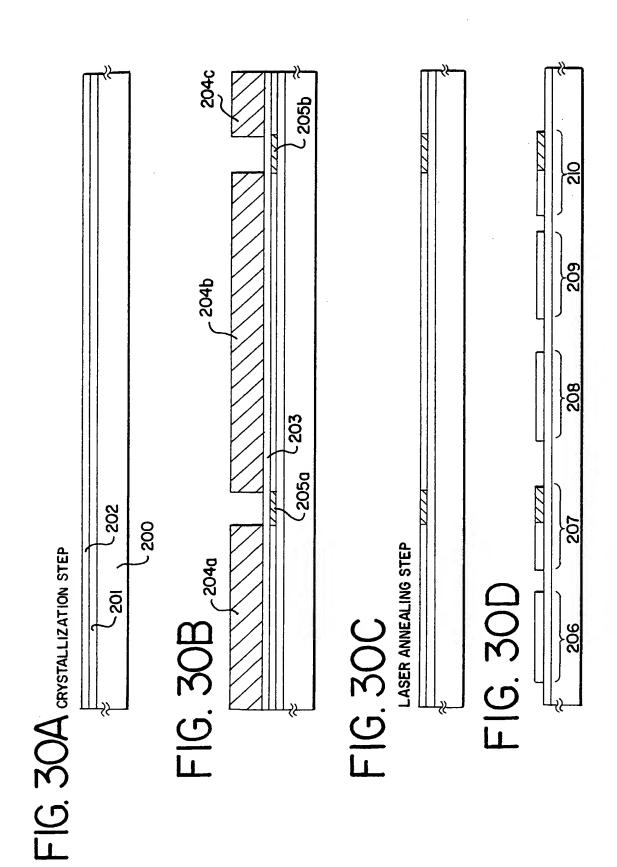
(J+I)-TH ROW FINAL RESETTING LINE 2921

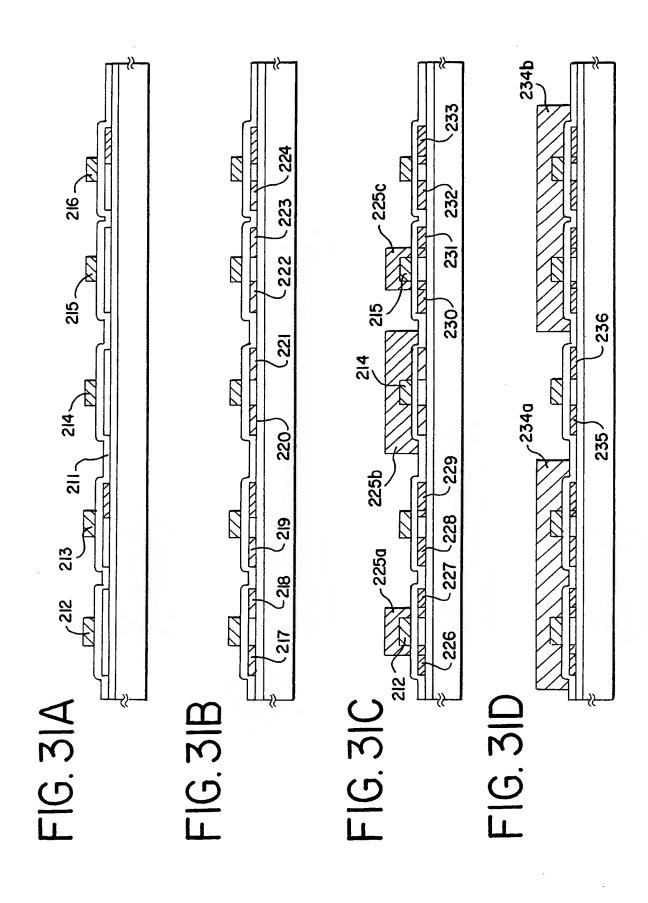
2918 (J+I)-TH ROW FINAL SELECTING LINE 2

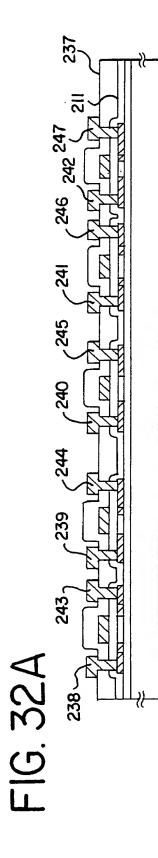
SIGNAL OUTPUT LINE DRIVER CIRCUIT
SCANNING PERIOD OF

FIG. 29









237 2487 FIG. 32B 249

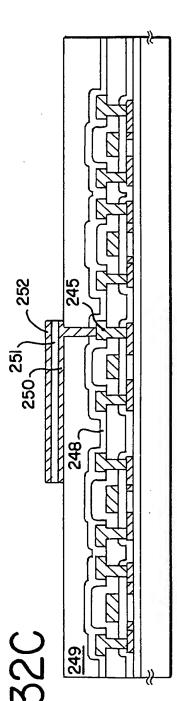


FIG. 32C

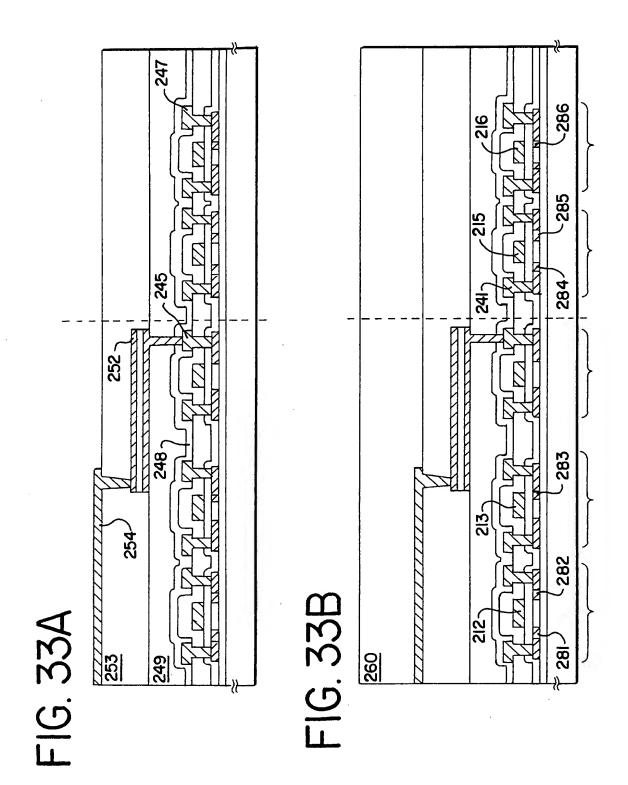


FIG. 34A

